Minimal and robust composite two-qubit gates with Ising-type interaction

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We construct a minimal robust controlled-NOT gate with an Ising-type interaction by which elementary two-qubit gates are implemented. It is robust against inaccuracy of the coupling strength and the obtained quantum circuits are constructed with the minimal number \(N = 3\) of elementary two-qubit gates and several one-qubit gates. It is noteworthy that all the robust circuits can be mapped to one-qubit circuits robust against a pulse length error. We also prove that a minimal robust SWAP gate cannot be constructed with \(N = 3\) but requires \(N = 6\) elementary two-qubit gates.

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I. INTRODUCTION

Gates robust against noise are essential for reliable computation irrespective of its nature being classical or quantum [1–4]. In quantum computation, one of the promising ways to achieve this goal is to employ composite gates [5,6]: realizing a target gate as a sequence of elementary gates so that systematic errors inherent in elementary gates cancel with each other.

It has been becoming clear that one must pay attention to both the noise and systematic errors, so as to materialize a working quantum computer. Indeed, in a layered architecture for quantum computing, physical qubits undergo the composite gate operations and dynamical decoupling as preliminary processing to quantum error correction, which compensates for bit flips and phase flips due to the stochastic noise [7].

Composite gates (pulses) have been developed in nuclear magnetic resonance (NMR), which is also a good test bed for processing to quantum error correction, which compensates for a two-qubit gate in NMR quantum computation depicted in Fig. 1. Then we construct minimal robust composite two-qubit gates that have no corresponding one-qubit gates. We claim that our gates are “minimal” in that all our minimal robust composite two-qubit gates have the corresponding one-qubit gates [10–12]. The remaining task is to construct robust two-qubit gates.

For two-qubit gates, an error in the parameter \(\Theta\) must be considered. This error may be caused by (i) inaccuracy of the numerical value of the coupling \(J\) or (ii) the influence of other qubits. It might also be caused by an error in gate execution time \(T\). Many composite gates have been proposed to suppress this error [13–17]. The composite gates proposed in [13] compensate for the error terms for \(S(\Theta)\) up to second order, whereas those proposed in [14] are applicable to more general interactions. However, these merits are realized at the cost of a longer execution time, which leads to less tolerance to stochastic noise: A shorter composite pulse is desirable.

All these two-qubit composite gates are designed following the idea that any one-qubit quantum circuit robust against the pulse length error is mapped to a two-qubit composite gate robust against the error in \(\Theta\). The mathematical basis of this approach is that \(SU(2)\) may be mapped to a proper subgroup of \(SU(4)\). Although this approach was successful, we might have ruled out robust two-qubit gates that have no corresponding one-qubit gates.

In this paper, we start from the most generic quantum circuit for a two-qubit gate in NMR quantum computation depicted in Fig. 1. Then we construct minimal robust composite two-qubit gates. We claim that our gates are “minimal” in that the obtained quantum circuits are constructed with a minimal number of two-qubit gates. Moreover, it is shown below that all our minimal robust composite two-qubit gates have the corresponding one-qubit composite gates.

This paper is organized as follows. In Sec. II, we introduce the circuit family that we are going to analyze. A robustness condition is also derived. In Sec. III, we apply it to the circuit family and obtain the simplest exact solutions. This solution realizes the robust CNOT gates but not the robust SWAP gates. However, we find a suitable combination of the solutions, which makes the robust gates equivalent to the SWAP gates up to one-qubit gates. In Sec. IV, we demonstrate an intriguing property of the solutions. All the composite two-qubit gates designed with our solutions have correspondence to one-qubit composite gates. By using this, we show that the solutions can be seen as extensions of well-known one-qubit composite gates. Section V is devoted to discussion and conclusions.
where $\mathbb{I}$ is the 2 × 2 identity matrix. The solution is $
abla^2 R_1 \otimes \nabla^2 R_1 = \pm \nabla^2 S_1$ with $\Theta_1 / \Theta_1 = \mp 1$. Then $U = \pm \nabla \otimes \nabla$, which implies that all the robust circuits belong to $\text{SU}(2)^{\otimes 2}$. This proves that a robust $S(\Theta)$ for generic $\Theta$ with $N = 2$ is impossible. We should try $N \geq 3$ to implement a nontrivial two-qubit gate robust against the error in $\Theta$.

### B. Minimal robust entangler with $N = 3$

The error term $\delta U$ for $N = 3$ is written explicitly as

\[
\delta U = \frac{1}{2} \left[ \Theta_1 \sigma_z^\otimes S_1 (\nabla \otimes \nabla) S_2 (\nabla \otimes \nabla) S_3 + \Theta_2 S_1 (\nabla \otimes \nabla) S_2 (\nabla \otimes \nabla) S_3 \right].
\]

Similarly to the $N = 2$ case, by using $R_i \sigma_z = \sigma_z R_i$, the robustness condition, (6), reads

\[
\Theta_2 (\nabla \otimes \nabla) + \Theta_1 S_2 (\nabla \otimes \nabla) = 0.
\]

We set $\phi_0 = \phi_1 = 0$ without loss of generality since we can apply $e^{i \epsilon \sigma_z} / \Theta_1$ to $U$ and freely adjust $\phi_2$ and $\phi_3$ without changing the robustness condition. The robustness condition, (10), reduces to

\[
\Theta_1 \nabla_2 + \Theta_2 \nabla_3 = 0,
\]

\[
\Theta_1 = \alpha \Theta_2 \sin \theta_2, \quad \Theta_3 = \beta \Theta_2 \sin \theta_2, \quad \Theta_4 = \beta \Theta_2 \sin \theta_2.
\]

For brevity, we introduced symbols $\alpha, \beta, \gamma \in \{\pm 1\}$, which are determined by $\Theta_1, \Theta_2$, and $\Theta_4$ (see Table I). Derivation of the above solution is given in the Appendix. Note that $\Theta_2 \neq 0$ for $U = S(\Theta)$ to be nontrivial. We list the properties of the solution, (12): First, the axes of the one-qubit gates are aligned: $\phi_i = \phi_1 = 0$. Second, this circuit has the shortest execution time when $\Theta_2 = 2 \pi$, since the execution time $T$ is proportional to $\sum_i \Theta_i / \Theta_2$. We also obtain the other class of general solutions by renaming the qubits as $i \leftrightarrow 2$.

The general solutions, (12), are apparently complicated due to various combinations of $\alpha, \beta, \gamma$. Nevertheless, the solution can be simplified, without loss of generality, as follows. Let us note that

\[
(\sigma_z \otimes \nabla) S(\Theta_1)_{\alpha} (\sigma_z \otimes \nabla) = S(\Theta_1)_{-\alpha}.
\]

### TABLE I. Two-value variables $\alpha, \beta, \gamma \in \{\pm 1\}$ in Eq. (12) as functions of $\Theta_1, \Theta_2, \Theta_4$. Note that $\Theta_2 \neq 0$.

<table>
<thead>
<tr>
<th>$\alpha$</th>
<th>$\Theta_1 / \pi$</th>
<th>$\beta$</th>
<th>$\Theta_2 / \pi$</th>
<th>$\gamma$</th>
<th>$\Theta_4 / \pi$</th>
</tr>
</thead>
<tbody>
<tr>
<td>Odd</td>
<td>$\gamma$</td>
<td>Odd</td>
<td>$\Theta_2 / \pi$</td>
<td>Even</td>
<td>$-1$ Odd</td>
</tr>
<tr>
<td>Even</td>
<td>$-\gamma$</td>
<td>Even</td>
<td>$\Theta_4 / \pi$</td>
<td>Even</td>
<td>$-1$ Odd</td>
</tr>
</tbody>
</table>
First, let us take the SWAP gate. Note that operation is realized by taking the robust CNOT gate. In the case of the shortest pulse, this resulting circuit has the minimal number of elementary gates that implements a composite gate $U_{\alpha}$, as shown in Fig. 2. We work with this circuit from now on.

We present the simplest, and thus the most useful, example that implements a composite gate $U = S(\Theta)$ for an arbitrary $\Theta$. First, let us take $\Theta_2 = 2\pi$ so that the execution time is shortest and $\gamma = -1$. We then take $\Theta_1 = 0$ to ensure $\alpha = -1$ and $\beta = -\gamma = 1$. Further, we put $\Theta_1 = \Theta_2 = \Theta \in [\pi/2, \pi]$, where $\Theta$ is a parameter chosen in such a way that $\Theta_1 = \Theta_2 = -\pi/2 \sec \theta \geq 0$ and that $R_{1}$ and $R_{2}$ are the shortest pulses. For this simplest composite pulse $U$, we find the identity

\[ I \otimes R(\eta,0)[I \otimes R(\eta,0)]^\dagger = S(4\zeta), \]

where $\eta$ and $\zeta$ are given by

\[ \tan \eta = \tan \sec \left(\frac{\pi}{2} \sec \theta\right), \quad \cos \zeta = \cos \theta \sin \left(\frac{\pi}{2} \sec \theta\right). \]  

Thus, up to one-qubit unitary operations, $U$ implements the operation $S(\pi) (\zeta = \pi/4)$. Since the Cartan decomposition of the CNOT gate contains $S(\pi)$, we observe that $U$ implements the robust CNOT gate. In the case of the shortest pulse, this operation is realized by taking

\[ \theta = \pi - \theta^*, \quad \theta^* \approx 0.674, \]

which has been obtained by solving the second of Eqs. (15) numerically with $\zeta = \pi/4$.

This simplest circuit, however, does not implement the SWAP gate. Note that

\[ S(4\zeta) = \cos \zeta 1^\otimes 2 - i \sin \zeta \sigma_z^\otimes 2, \]

while

\[ \text{SWAP} = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} = 1^\otimes 2 + \sigma_x^\otimes 2 + \sigma_y^\otimes 2 + \sigma_z^\otimes 2. \]

Since any one-qubit unitary operations leave the number of the summand on the right-hand side unchanged, $S(4\zeta)$ cannot be converted to the SWAP gate, which proves our claim A more precise argument is as follows: First, note that $[1, \sigma_x, \sigma_y, \sigma_z]$ forms an orthogonal basis, in the sense of the Hilbert-Schmidt inner product. By one-qubit unitary transformations, the orthonormal basis undergoes the change $[1, \sigma_x, \sigma_y, \sigma_z]^\otimes 2 \rightarrow V_1[1, \sigma_x, \sigma_y, \sigma_z]W_1 \otimes V_2[1, \sigma_x, \sigma_y, \sigma_z]W_2$, where $V_i, W_i \in SU(2)$. The new bases $V_1[1, \sigma_x, \sigma_y, \sigma_z]W_1$ are also orthogonal bases. We use these transformed bases to evaluate the number of summands after the one-qubit unitary operations. Note that this can also be shown by using operator Schmidt decomposition [18,19] or SU(2)$^\otimes 2$ invariants [20–22]. The above observation is proven to be valid also for the general solutions, (12): The $N = 3$ general solutions generate the robust composite pulses for $S(\Theta)$ only, while there are no such $N = 3$ solutions that make the SWAP gate robust.

C. Minimal robust SWAP gates

Instead of the SWAP gate, let us consider how to make a composite gate of the following two-qubit gate:

\[ V = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & -i & 0 \\ 0 & -i & 0 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} = 1^\otimes 2 - i\sigma_x^\otimes 2 - i\sigma_y^\otimes 2 + \sigma_z^\otimes 2. \]

To investigate the properties of $V$, let us introduce the operator Schmidt coefficients (OSC) and operator Schmidt number (OSN). The OSCs are coefficients of the basis operators appearing after the operator Schmidt decomposition is carried out.

The OSN stands for the number of nonzero OSCs [18,19]. Note that both the OSCs and the OSN are local unitary (LU) invariants. This implies that if the OSNs of two given operators are different from each other, then the two operators cannot be locally equivalent.

By using the OSCs, we find that the $V$ gate is LU equivalent to the SWAP gate, since the OSCs of $V$ are $[1/2, 1/2, 1/2, 1/2]$, which are identical to those of the SWAP gates as shown in Eq. (18). Thus we may identify the $V$ gate with the SWAP gate, up to LU operations.

Now let us evaluate how many elementary $S(\Theta)$ gates we need in order to construct the $V$ gate. Clearly it is impossible to construct the $V$ gate (and hence the SWAP gate) with only one elementary $S(\Theta)$ gate, as shown in the previous section. We can construct the $V$ gate with two elementary $S(\pi)$ gates as

\[ V = R\left(\frac{\pi}{2}, \frac{\pi}{2}\right)^\otimes 2 S(\pi) \left[R\left(\frac{\pi}{2}, \frac{\pi}{2}\right)^\otimes 2\right]^\dagger, \]

which is a minimal circuit of $V$ within the gate set, (1). By replacing two elementary $S(\pi)$ gates in Eq. (20) with the $N = 3$ composite $S(\pi)$ gates, (14), we find the $N = 2 \times 3 = 6$ robust $V$ gate.

Let us check whether there are smaller constructions of a robust $V$ gate based on circuit (20). According to Sec. III A, any $N = 2$ robust gate implements the trivial gate only. Thus, taking into account the fact that the two factors on the right-hand side of Eq. (20) implement nontrivial entangling gates, it turns out that we must use two $N = 3$ solutions, (14), for the robust implementation of $V$, totaling six elementary $S(\pi)$ gates. Furthermore, we find that in the resulting robust $V$
circuit, the subcircuit starting from the third elementary $S(\Theta)$ gate to the fourth cannot be merged to a single elementary $S(\Theta)$ gate with an appropriate value of $\Theta$, since the OSN of the subcircuit is 4, whereas that of the $S(\Theta)$ gate is 2. This observation shows that there exists no reduction to an $N = 5$ robust circuit. Next, we examine whether the subcircuit starting from the second elementary $S(\Theta)$ to the fourth can be merged to one elementary $S(\Theta)$ gate. Since the second $S(\Theta)$ gate is $S(2\pi) = -i\sigma_y^{\otimes 2}$, it cannot alter the OSN. This implies that the subcircuit under consideration has the same OSN as the subcircuit starting from the third to the fourth; thus, for the same reason as in the $N = 5$ case, we have no reduction to an $N = 4$ robust circuit.

IV. MAPPING TO ONE-QUBIT COMPOSITE PULSES

We can map solutions (12) to one-qubit composite pulses robust against a pulse length error. Here, the pulse length error is a systematic error, by which the rotation angle of a one-qubit gate is shifted as

$$R(\theta, \phi) \rightarrow R((1 + e)\theta, \phi).$$  \hspace{1cm} (21)

We consider generators

$$X = \sigma_z \otimes \sigma_z, \quad Y = \sigma_z \otimes \mathbf{u} \cdot \mathbf{\sigma}, \quad Z = \mathbb{I} \otimes \mathbf{v} \cdot \mathbf{\sigma}$$  \hspace{1cm} (22)

with $\mathbf{u} = (\sin \Theta, -\cos \Theta, 0)$, $\mathbf{v} = (\cos \Theta, \sin \Theta, 0)$, and $\mathbf{\sigma} = (\sigma_x, \sigma_y, \sigma_z)$. Here $\Theta$ is an arbitrary angle. Since $X$, $Y$, and $Z$ satisfy the same commutation relations for the generators of $SU(2)$, we recognize a correspondence among the generators

$$\sigma_x \leftrightarrow X, \quad \sigma_y \leftrightarrow Y, \quad \sigma_z \leftrightarrow Z.$$  \hspace{1cm} (23)

Therefore, given a one-qubit gate $R(\Theta/2, \theta)$, we can associate a rotation operation $R(\Theta/2, \theta)$ in a subgroup of $SU(4)$ by

$$R(\Theta/2, \theta) \leftrightarrow \exp[-i((\Theta/2)\cos \theta X + \sin \theta Y)/2]$$

$$= R(\Theta/2, \theta),$$  \hspace{1cm} (24)

under correspondence (23). Furthermore, the identity

$$R(\Theta/2, \theta) = [1 \otimes R(\Theta, \Omega)]S(\theta)[1 \otimes R(\Theta, \Omega)]^\dagger$$  \hspace{1cm} (25)

obtained from Eq. (22), shows that a pulse length error in $\Theta/2$ of $R(\Theta/2, \theta)$ is manifestly mapped to a $J$-coupling error in $\Theta$ of $S(\Theta)$ under this correspondence. Note that we have an infinite number of subalgebras parameterized by $\Omega$ in $\mathbf{u}$ and $\mathbf{v}$ and that we obtain the mapping Jones employed in [13] as a special case when $\Omega = \pi/2$.

Solutions (12) can be mapped locally to one-qubit composite pulses robust against a pulse length error by using this mapping. Let us introduce a free parameter $\Phi_1$ and define

$$\Phi_2 = \Phi_1 - \theta_1, \quad \Phi_3 = \Phi_2 - \theta_2,$$  \hspace{1cm} (26)

which clearly satisfy

$$R(\Phi_2, 0)^\dagger R(\Phi_1, 0) = R(\theta_1, 0),$$

$$R(\Phi_3, 0)^\dagger R(\Phi_2, 0) = R(\theta_2, 0).$$  \hspace{1cm} (27)

By using Eqs. (25) and (27), we find for a circuit $U$ in Fig. 2 the following identity:

$$[1 \otimes R(\Phi_1, 0)]U[1 \otimes R(\Phi_1, 0)]^\dagger$$

$$= [1 \otimes R(\Phi_1, 0)]S(\Theta)[1 \otimes R(\Phi_1, 0)]^\dagger$$

$$\times [1 \otimes R(\Phi_2, 0)]S(\Theta)[1 \otimes R(\Phi_2, 0)]^\dagger$$

$$\times [1 \otimes R(\Phi_1, 0)]S(\Theta)[1 \otimes R(\Phi_1, 0)]^\dagger$$

$$= \mathcal{R}(\Theta/2, \Phi_3)R(\Theta/2, \Phi_2)R(\Theta/2, \Phi_1).$$  \hspace{1cm} (28)

Since the mapping, (23), preserves the $su(2)$ algebra and the $J$-coupling error is mapped to the pulse length error, one-qubit circuit obtained by replacing $\mathcal{R}$ with $R$ in the circuit, (28), must be robust against the pulse length error.

Let us consider the simplest case: $\theta_2 = 2\pi$, $\theta_1 = \bar{\theta}_2 = 0$, and $\theta_1 = -\bar{\theta}_2 = -\theta$. Then $\Phi_3 = \Phi_1$ and we obtain a composite rotation gate in a subspace of SU(4):

$$\mathcal{R}(-\pi \sec \theta, 2) R(\pi, \Phi_2) R(-\pi \sec \theta, 2).$$  \hspace{1cm} (29)

A one-qubit gate obtained from (29) by replacing $\mathcal{R}$ with $R$ is a composite pulse sequence called SCROFULOUS [23]. It is easy to see that Eqs. (21) and (23) in [23] are satisfied by (29). The rotation angle $\Theta/2$ as a composite pulse is $\zeta$ defined in Eqs. (15), while $\Phi_1$ determines the rotation axis. Hence, when mapped, our solutions, (12), can be seen as the most general family of the minimal quantum circuits robust against the pulse length error and SCROFULOUS is a special case thereof.

The mapping between one- and two-qubit gates makes the fidelity of our two-qubit composite pulse and that of the corresponding one-qubit composite pulse exactly identical. Readers who are interested in the fidelity plots on the two-qubit gates for the simplest cases may refer to Fig. 2 in Ref. [23].

In contrast, all the $N = 6$ robust $V$ gates cannot be mapped to one-qubit robust circuits, because not all the rotation axes of one-qubit gates are aligned. A more precise account is as follows: First, recall that the mapping to one-qubit gate is defined through $\Theta$ in Eq. (25). Since we have two kinds of one-qubit gates $R(\pi/2, \pi/2)$ and $R(\pi/2, 0)$ in Eqs. (20), we cannot decompose the robust $V$ gate as Eq. (28) in such a way that we use the one-qubit gates with a unique $\Omega$. This implies that the mapping is not well-defined, showing the nonexistence of the mapping.

As shown in [13], one-qubit composite gates, e.g., broadband1 (BB1) and time-symmetric BB1 can be mapped to $N = 4$ and $N = 5$ composite two-qubit gates, respectively. Although these BB1 analogues compensate for the error terms up to the second order, they fail to implement the SWAP gates, since the (time-symmetric) BB1 implements $R(\Theta, \theta)$, which is, from Eq. (24), mapped to $R(\Theta, \theta)$. This means that they implement the robust $S(\Theta)$ only, which supports the fact that we have no $N = 4, 5$ composite SWAP gates.

Final remarks are in order. We address that our $N = 3$ robust two-qubit $S(\Theta)$ gates are geometric quantum gates that utilize the Aharonov-Anandan phase [24]. This is because all the two-qubit robust gates obtained here are mapped to one-qubit composite gates robust against a pulse length error, which are proved to be geometric gates [17,25]. In general, however, the robust $V$ gates are not necessarily
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geometric, since there is no mapping to one-qubit circuits robust against the pulse length error; they happen to be geometric if we implement $R(\pi/2, \pi/2)$ and $R(\pi/2, 0)$ in Eqs. (20) by geometric composite gates robust against the pulse length error.

V. SUMMARY

In summary, we have found all two-qubit quantum circuits robust against the error with respect to $\Theta$ in the minimal setup ($N = 3$) and have shown that these were mapped to one-qubit composite pulses robust against the pulse length error. Our analysis clarified that the robustness condition introduces severe restriction on the circuits realized. For example, the SWAP gate cannot be robust in this minimal setting.

Although constructed on the Ising-type interaction, our pulse sequence may compensate for errors in generic two-qubit interaction, if it is nested with the “term isolation sequence” in the similar way to [14]. The sequence proposed in [14] is composed by nesting two two-qubit pulse sequences. One is the term isolation sequence, which reduces the generic coupling or the gate execution time. This nesting is applicable to our sequences and hence the resulting sequence may compensate for the interaction errors in various physical systems.

Furthermore, we have shown that the $N = 6$ composite gate is LU equivalent to the SWAP gate is composed of two minimal composite gates. The resulting composite gate is minimal and has no counterparts in the one-qubit composite gates; besides, they are not geometric in general. These contrasts between the CNOT and the SWAP gates are of interest and should be investigated further.

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APPENDIX: GENERAL SOLUTIONS OF THE $N = 3$ ROBUSTNESS CONDITION

We solve Eqs. (11) to find nontrivial solutions, (12). First, from Eqs. (11c) and (11b), we obtain

$$\Theta_2 = 2\pi k, \quad k \in 0,1,2, \ldots, \quad (A1)$$

with

$$\phi_1 = \pi m, \quad m \in \mathbb{Z} \quad (A2)$$

and

$$\tilde{\phi}_1 = \pi l, \quad l \in \mathbb{Z}. \quad (A3)$$

By substituting Eqs. (A1) and (A3) into Eqs. (11), we find

$$\Theta_1 \tilde{c}_1 c_1 = -2\pi k - \Theta_3 \tilde{c}_2 c_2, \quad (A4a)$$
$$(-1)^m \Theta_1 \tilde{s}_1 s_1 = (-1)^l \Theta_3 \tilde{s}_2 s_2, \quad (A4b)$$
$$(-1)^m \Theta_1 \tilde{s}_1 s_1 = (-1)^k \Theta_3 \tilde{c}_2 c_2, \quad (A4c)$$
$$(-1)^i \Theta_1 \tilde{s}_1 c_1 = (-1)^k \Theta_3 \tilde{c}_2 c_2. \quad (A4d)$$

We derive

$$\Theta_1 \Theta_3 \tilde{s}_1 s_2 \sin[\tilde{\theta}_1 + (-1)^i + \tilde{\theta}_2] = 0 \quad (A5)$$

by multiplying the right-hand (left-hand) side of Eq. (A4b) by the left (right) side of Eq. (A4c). The solutions are classified into four cases.

Case 1. When $\Theta_1 \Theta_3 = 0$, the quantum circuit results in that for $N = 2$ or less. From the previous argument, we find $U \in SU(2)^{\otimes 2}$; this case cannot implement $S(\Theta)$.

Case 2. When $\Theta_1 \Theta_3 \neq 0$ and $s_1 = 0$, we obtain $s_2 = 0$ by using Eqs. (A4b) and (A4c). By introducing $c_1 = (-1)^p$ and $c_2 = (-1)^q$ with $p,q \in \mathbb{Z}$, Eqs. (A4) reduce to

$$(-1)^p \Theta_1 \tilde{c}_1 = -2\pi k - (-1)^q \Theta_3 \tilde{c}_2, \quad (A6a)$$
$$(-1)^{i+p} \Theta_1 \tilde{s}_1 = (-1)^{k+q} \Theta_3 \tilde{s}_2. \quad (A6b)$$

We solve Eqs. (A6) with respect to $\Theta_1$ and $\Theta_3$ to obtain

$$\Theta_1 = -(-1)^p 2\pi k \tilde{s}_2 \csc(\tilde{\theta}_2 + (-1)^k \tilde{\theta}_1), \quad (A7a)$$
$$\Theta_3 = -(-1)^{i+q} 2\pi k \tilde{s}_2 \csc(\tilde{\theta}_2 + (-1)^k \tilde{\theta}_1). \quad (A7b)$$

Case 3. When $\Theta_1 \Theta_3 \neq 0$ and $s_1 = 0$, we find $s_1 = 0$ from Eqs. (A4b) and (A4c). Hence this case boils down to Case 2, and the solution is given by Eqs. (A7).

Case 4. When $\Theta_1 \Theta_3 s_1 s_2 \neq 0$, we find

$$\tilde{\theta}_1 + (-1)^i \tilde{\theta}_2 = \pi p, \quad p \in \mathbb{Z} \quad (A5)$$

from Eq. (A5). By using this in Eqs. (A4), we obtain

$$\tilde{c}_2((-1)^p \Theta_1 c_1 + \Theta_3 c_2) = -2\pi k, \quad (A8a)$$
$$\tilde{s}_2((-1)^{i+q} \Theta_1 s_1 + \Theta_3 s_2) = 0, \quad (A8b)$$
$$\tilde{c}_2((-1)^{i+q} \Theta_1 s_1 + \Theta_3 s_2) = 0, \quad (A8c)$$
$$\tilde{s}_2((-1)^p \Theta_1 c_1 + \Theta_3 c_2) = 0, \quad (A8d)$$

since $\tilde{c}_1 = (-1)^p \tilde{c}_2$ and $\tilde{s}_1 = (-1)^q \tilde{s}_2$.

We further use the following classification to find the solutions of Eqs. (A8):

4a. When $\tilde{s}_2 = 0$, we have $\tilde{c}_2 = (-1)^q$ with $q \in \mathbb{Z}$ and

$$(1)^{i+q} \Theta_1 s_1 - \Theta_3 s_2 = 0 \quad (A9)$$

from Eq. (A8c). Together with Eq. (A8a) we then find

$$\Theta_1 = -(-1)^{i+q} 2\pi k \tilde{s}_2 \csc(\tilde{\theta}_2 + (-1)^k \tilde{\theta}_1), \quad (A9a)$$
$$\Theta_3 = -(-1)^{i+q} 2\pi k \tilde{s}_2 \csc(\tilde{\theta}_2 + (-1)^k \tilde{\theta}_1). \quad (A9b)$$

4b. When $\tilde{s}_2 \neq 0$, we obtain $k = 0$. From Eq. (A1), then, we observe that the quantum circuit reduces to that for $N = 2$. Hence $U \in SU(2)^{\otimes 2}$ for the robust circuits.

After all we find two classes of nontrivial solutions. The first class is summarized in (12), which is obtained in Case 4a. Here, we take $\tilde{\phi}_1 = \phi_1 = 0$ in Eqs. (A3) and (A2) so that $l = m = 0$ in Eqs. (A10), since other cases reduce to this case by using the identity $R(\theta, l \pi) = R((-1)^i \theta, 0)$. We also obtain the other class of general solutions by renaming the qubits 1 ↔ 2.


